

GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

ELECTRICAL ENGG. DEPARTMENT

Ref. No.: EE/92

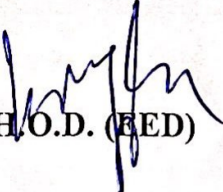
Dated: 17.01.2020

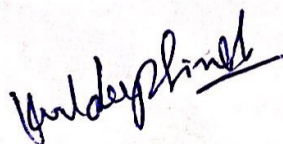
NOTICE

Following is the schedule for displaying Answer Booklets to students for the End Semester Examinations Nov, 2019.

Sr. No.	Semester	Section	Date	Time	Venue
1.	7 th (Reg. & Rp)	A & B	22.01.2020	8.30 AM	Power Electronics Lab
2.	5 th & 6 th (Reg. & Rp.)	A	22.01.2020	8.30 AM	Electrical Computation Lab
3.	5 th & 6 th (Reg. & Rp.)	B	22.01.2020	12.30 PM	Electrical Computation Lab
4.	3 rd & 4 th (Reg. & Rp.)	A	22.01.2020	8.30 AM	M & PLC Lab
5.	3 rd & 4 th (Reg. & Rp.)	B	22.01.2020	12.30 PM	M & PLC Lab
6.	M.Tech. (Reg. & Rp.)	(FT & PT)	22.01.2020	8.30 AM	Power Electronics Lab

- Students are directed to obey the time schedule strictly. Late comers will not be entertained.
- No student is allowed to bring Pen or Mobile during display of answer booklets.
- All students must mark attendance on stipulated time.
- No case of re-evaluation will be accepted after due date i.e. 22.01.2020.


H.O.D. (EED)



Distribution:

1. C.O.E.
2. Dean Academics
3. DNB
4. HNBS
5. College Website