

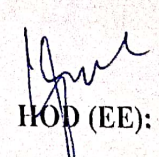
GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA
ELECTRICAL ENGINEERING DEPARTMENT
 Detainee Time table (Odd Sem.)
 (Session July 2025 – Dec 2025)

Pf. Na EE/949

Dated: 9/9/2025

Semester	Subject Code	Faculty Name	Mon	Tues	Wed	Thurs	Fri	Faculty Sign
1st	Manufacturing Practices (EESC104)	Pf. Ranjit Singh	Along with the classes of first year					
	Programming for Problem Solving (EESC103)	Pf. Kuljit Kaur (CSE)						
	Economics (HSMC102)	Pf. Jasmine Kaur (App. Sc.)						
4th	Signals and Systems (PCEE-108)	Pf. Sukhpal Singh		S & S L (2:30 PM) Adv Meas. Lab	S & S L (3:30 PM) Adv Meas. Lab		S & S L (3:30 PM) Adv Meas. Lab	
	Seminar and Technical Report Writing (PREE-101)	Pf. Samreet Kaur	S & TRW P (2:30-4:30 PM) S & R LAB		S & TRW P (2:30-4:30 PM) S & R LAB			
	Power Electronics (PCEE-107)	Dr. Ranvir Kaur	PE L (2:30 PM) G5			PE L (2:30 PM) G4	PE L (3:30 PM) G5	
5th	Power Systems – I (Apparatus and Modelling) (PCEE-109)	Pf. Swapandeep Kaur	PS-I L (3:30 PM) PS Lab			PS-I L (3:30 PM) PS Lab	PS-I L (3:30 PM) PS Lab	
	Electric Generation and Economics (PCEE-113)	Pf. Kuldeep Singh		EGE L (3:30 PM) NCES Lab	EGE L (3:30 PM) NCES Lab	EGE L (2:30 PM) S & R Lab		
	Microprocessors and Microcontrollers (PCEE-111)	Pf. Gagandeep Singh Sodhi		MP & MC L (3:30 PM) MP & MC Lab		MP & MC L (2:30 PM) E. M/c's Lab	MP & MC L (2:30 PM) E. M/c's Lab	
	Control Systems (PCEE-110)	Pf. Ravinder Kaur			CS L (3:30 PM) PE Lab	CS L (3:30 PM) PE Lab	CS L (3:30 PM) PE Lab	

I/c time-table: 

HOD (EE): 

1. Applied Sc.
2. CSE