

## Minutes of Meeting, Board of Studies, 15.7.16

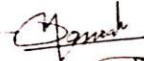
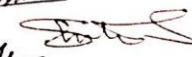
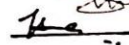
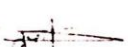
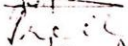


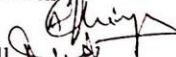

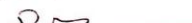
GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA  
ELECTRICAL ENGINEERING DEPARTMENT

Minutes of Meeting (Board of Studies)

Held on 15<sup>th</sup> July, 2016

Venue: Committee Room

The following members were present:

1. Dr. Sanjay Marwaha - 
2. Er. Pritam Singh - 
3. Pf. Jaswinder Singh - 
4. Dr. Y.S.Brar
5. Pf. Khushdeep Singh - 
6. Pf. Preetinder Singh - 
7. Dr. Kanwardeep Singh - 
8. Pf. Gagandeep Singh Sodhi - 
9. Pf. Arvind Dhingra - 
10. Pf. Harmeet Singh Gill - 
11. Pf. Rupinderjit Singh - 

Pf. Jaswinder Singh, Chairman, BoS welcomed the participants to the meeting. The agenda which had already been circulated was placed before the board.

1. The syllabus for 5<sup>th</sup> & 6<sup>th</sup> Semester Electrical Engineering was put up for discussion and approval. The credit based scheme was put forward. The board recommended that chapter wise weightage should be specified in syllabus. The board after certain minor amendments unanimously approved the same.

**APPROVED**

2. The study scheme for 7<sup>th</sup> & 8<sup>th</sup> Semester Electrical Engineering was put up for discussion. The scheme was approved.

**APPROVED**

3. The board members were of the view that syllabus for 7<sup>th</sup>/8<sup>th</sup> semester should be ready before December, 2016.

**AGREED**

There being no other matter, the meeting ended with a vote of thanks.

  
Chairman, BoS(EED)

Cc: Director: for kind information  
Dean (Academics)  
All members of BoS(EED)